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CONFIRMATION NO. 1899

Bib Data Sheet

SERIAL NUMBER 10/663,624	FILING DATE 09/16/2003 RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. 5646-114
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APPLICANTS
 Paul Murtagh, Duluth, GA;

** CONTINUING DATA *None and* *****

** FOREIGN APPLICATIONS *None and* *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 12/09/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY GA	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
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Verified and Acknowledged *[Signature]* Examiner's Signature *[Initials]*

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TITLE
 Delay-locked loop (DLL) integrated circuits having high bandwidth and reliable locking characteristics

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